2226P

Amendments to the Claims

This listing of claims will replace all prior version, and listings, of claims in the application.

Listing of Claims:

1.   (currently amended) A method for reducing the boot time for a computer, comprising ~~the steps for~~:

(a)   supplying power to the computer when the computer is in a powered down state;

(b)   disabling a plurality of input/output (I/O) devices coupled to the computer;

(c)   performing a boot process for the computer, comprising setting a flag by a basic input/output system (BIOS) of the computer, wherein setting the flag indicates that the computer is being booted from a powered down state; and

(d)   placing the computer in a suspend to memory state, wherein the steps (a) through (d) are performed before a user turns on the computer, wherein the placing comprises:

(d1)   checking the flag by an operating system (OS) of the computer, wherein the flag indicates whether or not the computer is being booted from the powered down state, and

(d2)   placing the computer in the suspend to memory state if the flag indicates that the computer is being booted from the powered down state.

2.   (canceled)

3.   (currently amended) The method of claim 1, wherein the supplying ~~step~~ (a) comprises:

(a1)   supplying power to the computer by plugging the computer into an AC outlet.

4.   (canceled)

5.   (previously presented) The method of claim 1, wherein the flag comprises at least one chip set register.

6.   (canceled)

7.   (original) The method of claim 1, wherein the suspend to memory state is an S3 state.

8.   (previously presented) The method of claim 1, further comprising:

(e)   supplying power to the computer when the computer is in the suspend to memory state;

(f)   resuming operation of an OS of the computer;

(g)   checking the flag by the OS, wherein the flag indicates whether or not the computer is being booted from the powered down state;

(h)   enabling the plurality of I/O devices if the flag indicates that the computer is not being booted from the powered down state; and

(i)   operating the computer in a wake state.

9.   (original) The method of claim 8, further comprising:

(j)   returning the computer to the suspend to memory state if the computer is being turned "off".

10.   (original) The method of claim 9, wherein the returning step (j) comprises:

(j1)   returning the computer to the suspend to memory state if a power button of the computer is pressed.

11.   (currently amended) A method for reducing the boot time for a computer, comprising ~~the steps for~~:

(a)   supplying power to the computer;

(b)   determining if the power is supplied to the computer when the computer is in a powered down state or a suspend to memory state;

(c)   booting the computer when the power is supplied to the computer when the computer is in a powered down state, wherein the booting step (c) comprises:

(c1)   disabling a plurality of I/O devices coupled to the computer,

(c2)   performing a boot process for the computer, comprising setting a flag by a BIOS of the computer, wherein the flag indicates whether or not the computer is being booted from the powered down state, and

(c3)   placing the computer in the suspend to memory state, wherein ~~the steps~~ (c1) through (c3) are performed before a user turns on the computer, wherein the placing is performed by an OS of the computer, wherein the placing comprises:

(c3i)   checking the flag by the OS, wherein the flag indicates whether or not the computer is being booted from the powered down state, and

(c3ii)   placing the computer in the suspend to memory state if the flag indicates that the computer is being booted from the powered down state; and

(d)   operating the computer in a wake state if the power is supplied to the computer when the computer is in the suspend to memory state.

12.   (canceled)

13.   (canceled)

14.   (previously presented) The method of claim 11, wherein the flag comprises at least one chip set register.

15.   (currently amended) The method of claim 11, wherein the operating step (d) comprises:

(d1)   resuming operation of an OS of the computer;

(d2)   checking the flag by the OS, wherein the flag indicates whether or not the computer is being booted from the powered down state;

(d3)   enabling the plurality of I/O devices if the flag indicates that the computer is not being booted from the powered down state; and

(d4)   operating the computer in the wake state.

16.   (original) The method of claim 11, further comprising:

(e)   returning the computer to the suspend to memory state if a power button of the computer is pressed.

17.   (currently amended) A computer readable medium with program instructions for reducing the boot time for a computer, comprising the instructions for:

(a)   supplying power to the computer when the computer is in a powered down state;

(b)   disabling a plurality of I/O devices coupled to the computer;

(c)   performing a boot process for the computer, comprising setting a flag by a basic input/output system (BIOS) of the computer, wherein setting the flag indicates that the computer is being booted from a powered down state; and

(d)   placing the computer in a suspend to memory state, wherein the instructions (a) through (d) are performed before a user turns on the computer, wherein the placing comprises:

(d1)   checking the flag by an operating system (OS) of the computer, wherein the flag indicates whether or not the computer is being booted from the powered down state, and

(d2)   placing the computer in the suspend to memory state if the flag indicates that the computer is being booted from the powered down state.

18.   (canceled)

19.   (previously presented) The medium of claim 17, wherein the supplying instruction (a1) comprises instructions for:

(a1i)   supplying power to the computer by plugging the computer into an AC outlet.

20.   (canceled)

21.   (previously presented) The medium of claim 17, wherein the flag comprises at least one chip set register.

22.   (canceled)

23.   (original) The medium of claim 17, wherein the suspend to memory state is an S3 state.

24.   (previously presented) The medium of claim 17, further comprising instructions for:

(e)   supplying power to the computer when the computer is in the suspend to memory state;

(f)   resuming operation of an OS of the computer;

(g)   checking the flag by the OS, wherein the flag indicates whether or not the computer is being booted from the powered down state;

(h)   enabling the plurality of I/O devices if the flag indicates that the computer is not being booted from the powered down state; and

(i)   operating the computer in a wake state.

25.   (original) The medium of claim 24, further comprising instructions for:

(j)   returning the computer to the suspend to memory state if the computer is being turned "off".

26.   (original) The medium of claim 25, wherein the returning instruction (j) comprises instructions for:

(j1)   returning the computer to the suspend to memory state if a power button of the computer is pressed.

27.   (currently amended) A computer readable medium with program instructions for

reducing the boot time for a computer, comprising the instructions for:

(a)   supplying power to the computer;

(b)   determining if the power is supplied to the computer when the computer is in a powered down state or a suspend to memory state;

(c)   booting the computer when the power is supplied to the computer when the computer is in a powered down state, wherein the booting instruction (c) comprises instructions for:

(c1)   disabling a plurality of I/O devices coupled to the computer,

(c2)   performing a boot process for the computer, comprising setting a flag by a BIOS of the computer, wherein the flag indicates whether or not the computer is being booted from the powered down state, and

(c3)   placing the computer in the suspend to memory state, wherein the instructions (c1) through (c3) are performed before a user turns on the computer, wherein the placing is performed by an OS of the computer, wherein the placing comprises:

(c3i)   checking the flag by the OS, wherein the flag indicates whether or not the computer is being booted from the powered down state, and

(c3ii)   placing the computer in the suspend to memory state if the flag indicates that the computer is being booted from the powered down state; and

(d)   operating the computer in a wake state if the power is supplied to the computer when the computer is in the suspend to memory state.

28.   (canceled)

29.   (canceled)

30.   (previously presented) The medium of claim 27, wherein the flag comprises at least one chip set register.

31.   (previously presented) The medium of claim 27, wherein the operating instruction (d) comprises instructions for:

(d1)   resuming operation of an OS of the computer;

(d2)   checking the flag by the OS, wherein the flag indicates whether or not the computer is being booted from the powered down state;

(d3)   enabling the plurality of I/O devices if the flag indicates that the computer is not being booted from the powered down state; and

(d4)   operating the computer in the wake state.

32.   (original) The medium of claim 27, further comprising instructions for:

(e)   returning the computer to the suspend to memory state if a power button of the computer is pressed.

33.   (currently amended) A system, comprising:

a plurality of I/O devices; and

a computer coupled to the plurality of I/O devices, the computer comprising:

a BIOS,

a memory, and

an OS, wherein when power is supplied to the computer when the computer is in a powered down state before a user turns on the computer, the BIOS disables the plurality of I/O

devices and performs a boot process for the computer, and the OS places the computer in a

suspend to memory state, and

a register, wherein a state of the register indicates whether or not the computer is

being supplied power with the computer being in a powered down state or the suspend to

memory state,

wherein if the register indicates that the computer is being supplied power with

the computer being in a powered down state, then the OS places the computer in the suspend to

memory state,

wherein if the register indicates that the computer is being supplied power with

the computer being in the suspend to memory state, then the OS operates the computer in a wake

state.

34.   (canceled)

35.   (canceled)

36.   (canceled)